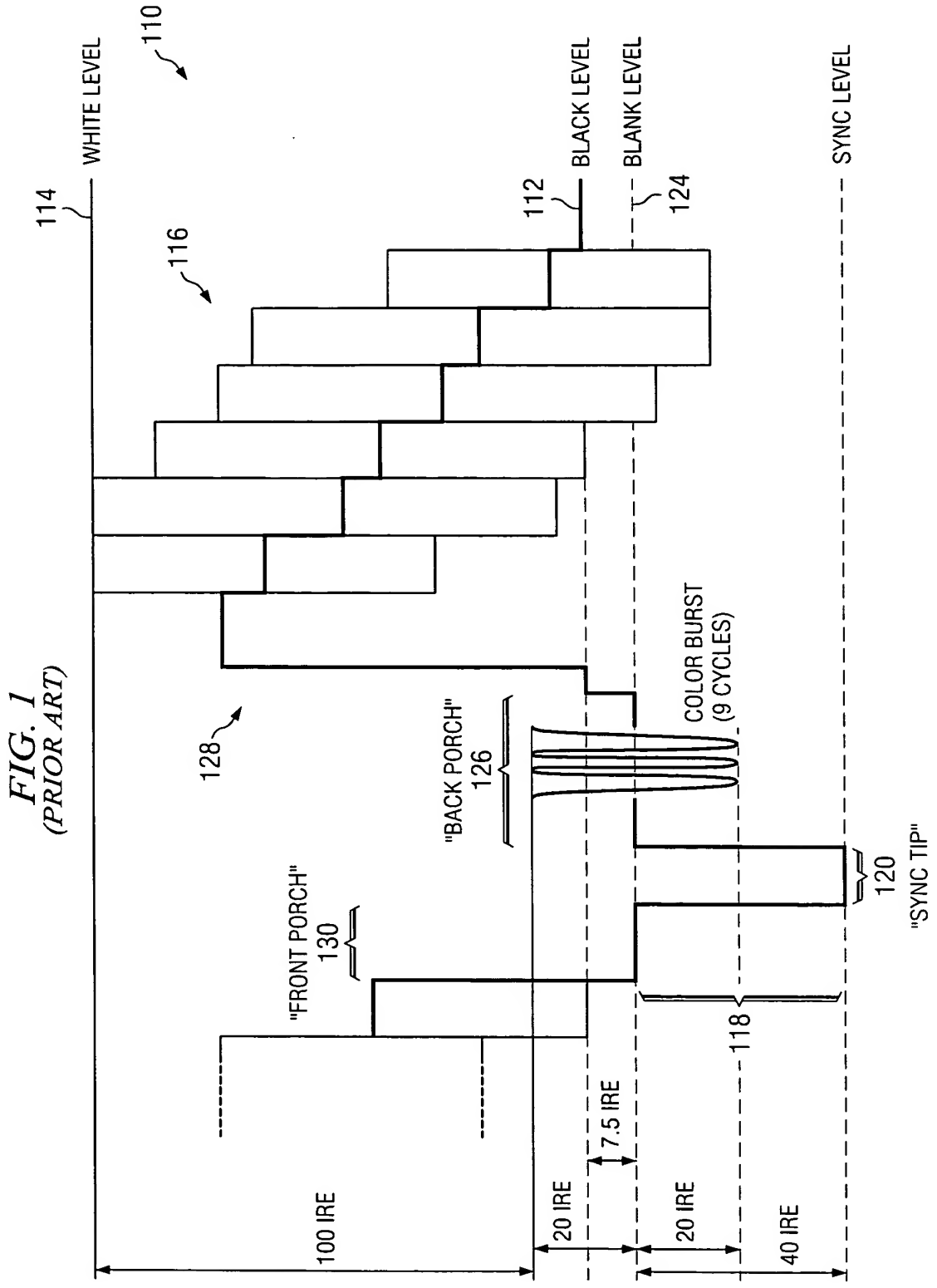

FIG. 1
(PRIOR ART)

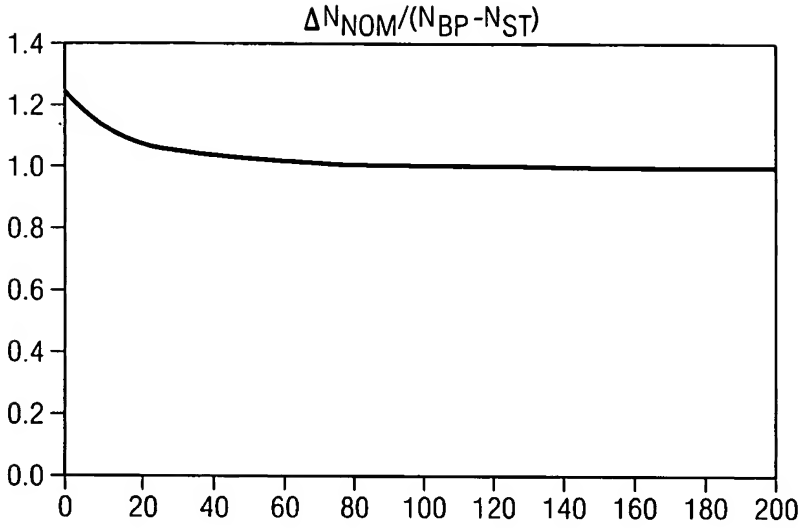

FIG. 2
(PRIOR ART)

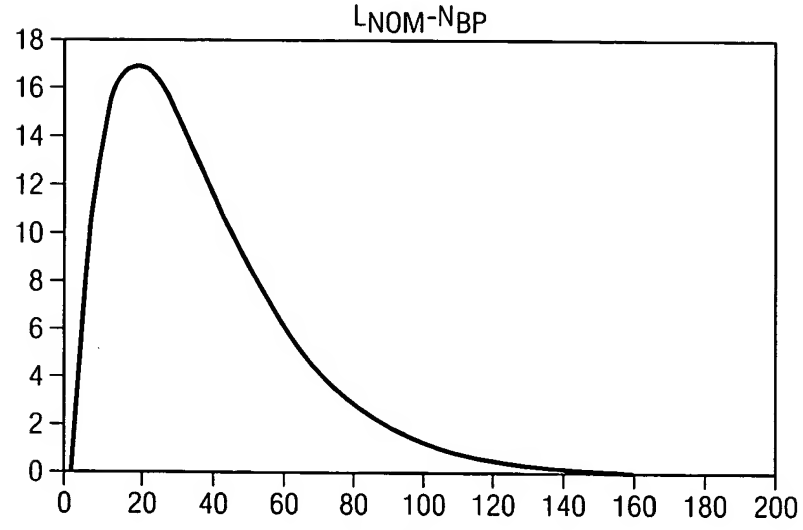

FIG. 3
(PRIOR ART)

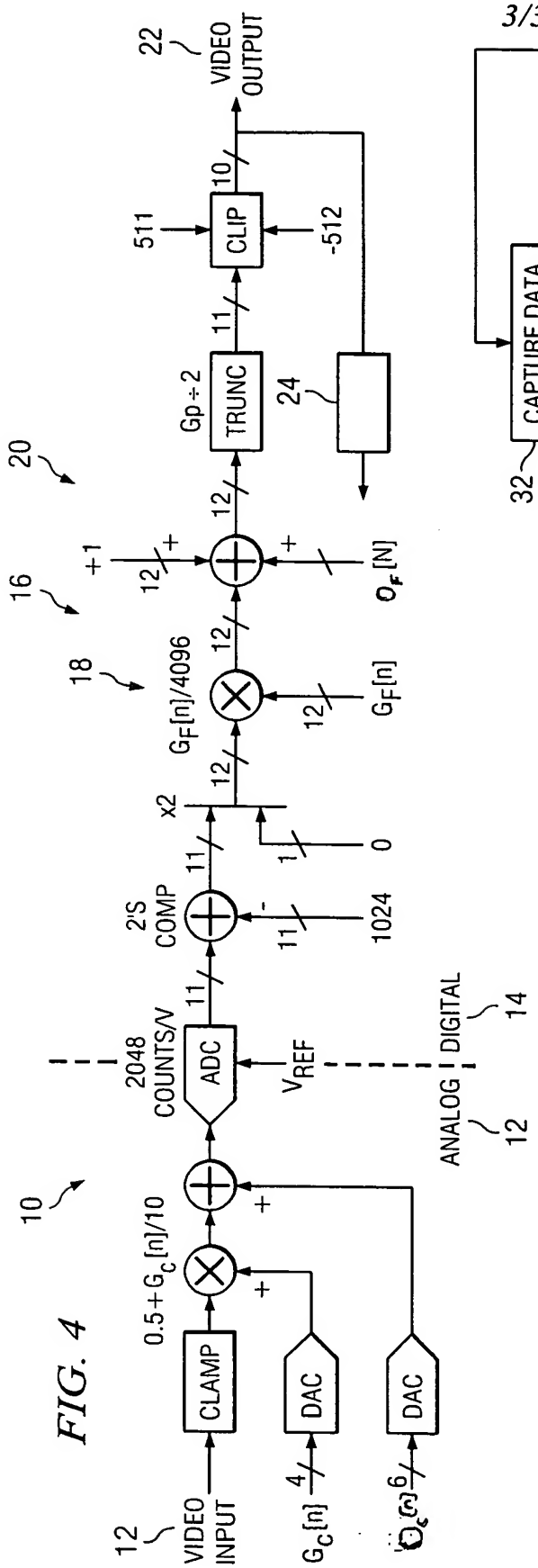
10
12
VIDEO INPUT
CLAMP
0.5+Gc[n]/10
Gc[n]
4
DAC
Oc[n]
6
DAC
2048 COUNTS/V
ADC
VREF
ANALOG
DIGITAL
12
14
2'S COMP
1024
11
x2
0
1
GF[n]/4096
GF[n]
12
16
18
+1
OF[N]
20
Gp÷2
TRUNC
24
511
CLIP
-512
22
VIDEO OUTPUT

FIG. 4

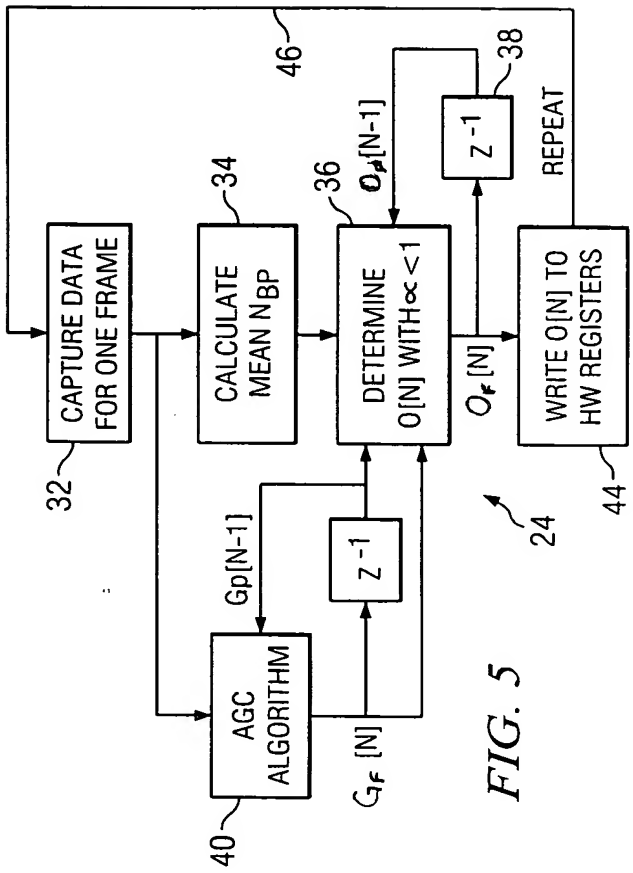
CAPTURE DATA FOR ONE FRAME
32
CALCULATE MEAN NBP
34
DETERMINE O[N] WITH ∝<1
36
OF[N-1]
Z-1
38
REPEAT
46
WRITE O[N] TO HW REGISTERS
44
OF[N]
24
Gp[N-1]
Z-1
AGC ALGORITHM
40
GF[N]

FIG. 5